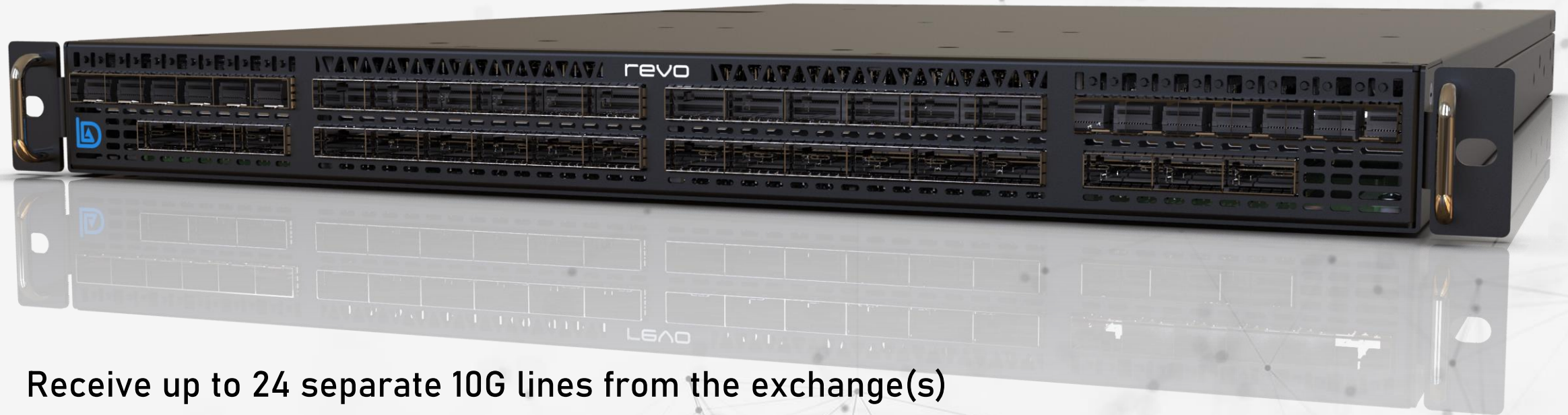


LDA TECHNOLOGIES™

Introducing LDA Revo

A specialized high-density device for line arbitration

Use Case



- Receive up to 24 separate 10G lines from the exchange(s)
- Deliver the data from each line to 12 FPGA boards
- Enable each FPGA board to send data to any line
- **At least 312 (!) 10G links: $24 + 24*12 = 312$**

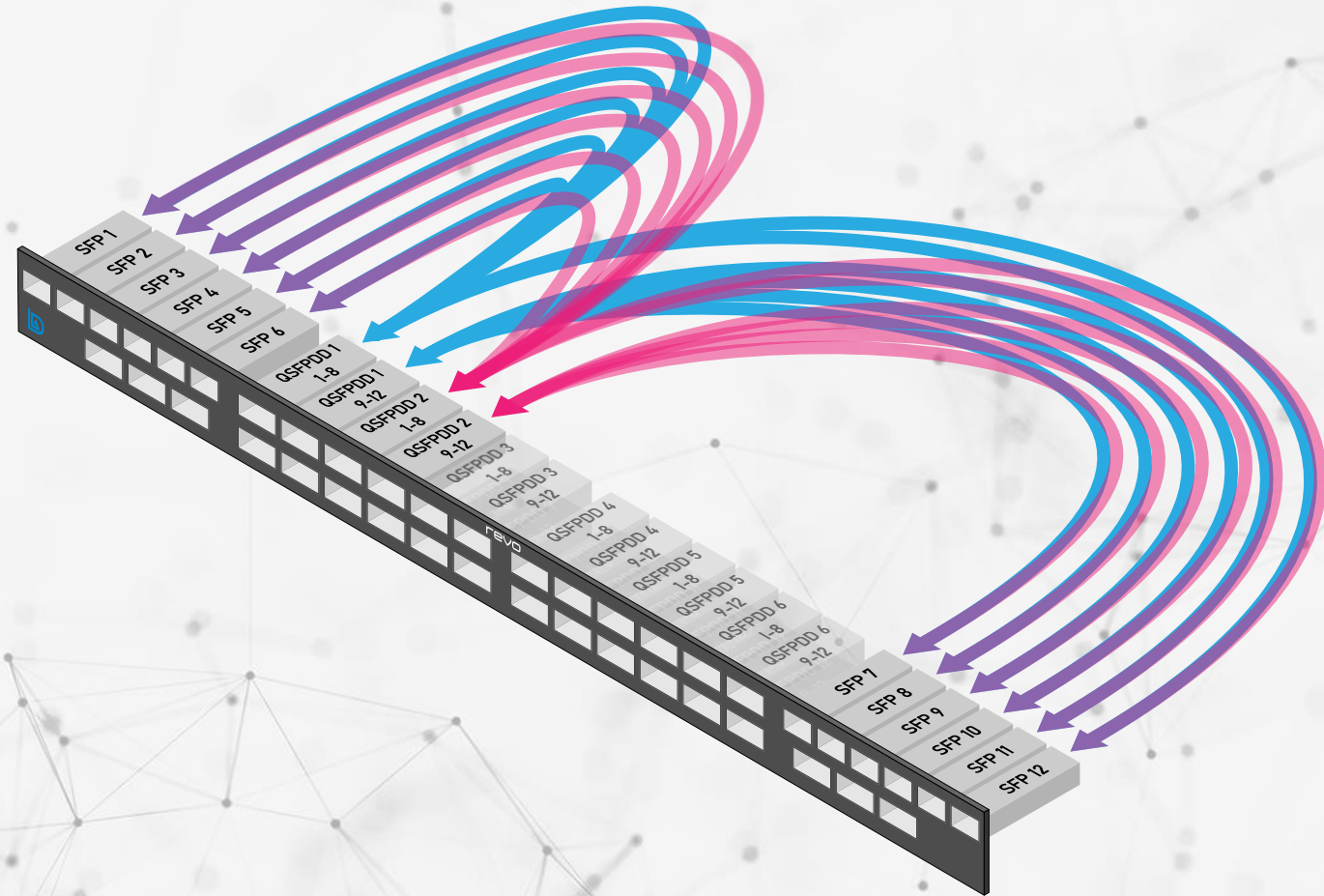
LDA Revo

- 12x SFP+ ports for exchange connectivity
- 24x QSFPDD ports (12 port pairs) for downstream (FPGA board) connectivity
- 6x QSFPDD ports for housekeeping/management tasks or external timestamping/capture



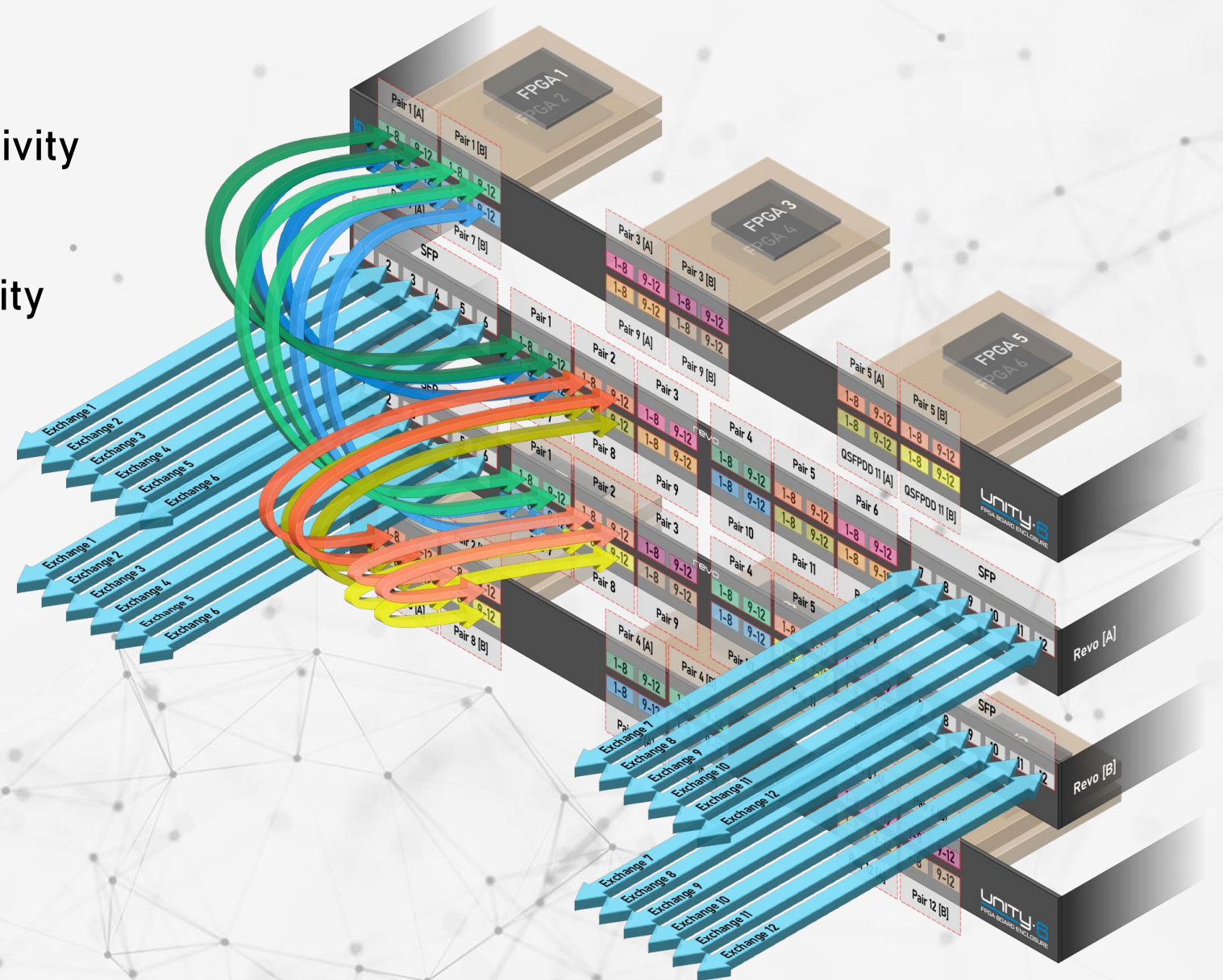
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- Configurable Layer 1 for SFP+ output



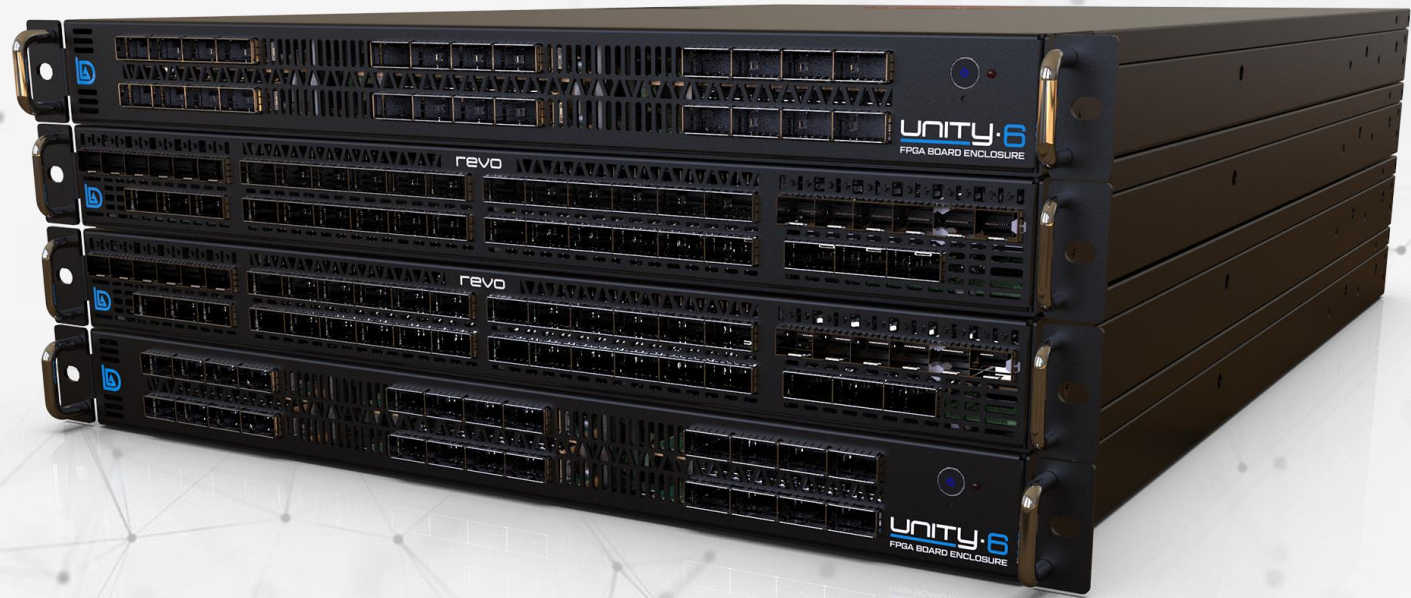
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- Downstream interconnect with 2"-7" QSFPDD copper cables



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- Configurable Layer 1 for SFP+ output
- Downstream interconnect with 2"-7" QSFPDD copper cables
- Internal tap aggregation and timestamping option with the complete LDA NeoTap features for capture and monitoring purposes.



Vinaka Vinaka
 Dankscheen Dankscheen
 Maake Maake
 Asante Asante
 Shukria Shukria
 Շնորհակալություն Շնորհակալություն
 Manana Manana
 Dankon Dankon
 Matondo Matondo
 감사합니다 감사합니다
 Kam Sah Hammida Kam Sah Hammida
 ار كاش ار كاش
 Mauruuru Mauruuru
 Biyan Biyan
 Diolch i Chi Diolch i Chi
 Terima Kasih Terima Kasih
 Taiku Taiku
 Tack Tack
 Blagodaram Blagodaram
 Ngiyabonga Ngiyabonga
 Dziekuje Dziekuje
 Juspaxar Juspaxar
 Chokrane Chokrane
 Arigato Arigato
 Grazie Grazie
 Tack Tack
 Mochchakkeram Mochchakkeram
 Gracias Gracias
 Tingki Tingki
 Gratias Tibi Gratias Tibi
 Obrigado Obrigado
 ありがとう ありがとう
 Dēkuji Dēkuji
 Nirringrazzjak Nirringrazzjak
 Hvala Hvala
 Welalin Welalin
 Di Ou Mèsi Di Ou Mèsi
 Kia Ora Kia Ora
 Kop Khun Khap Kop Khun Khap
 Paldies Paldies
 Djiere Dieuf Djiere Dieuf
 Eskerrik Asko Eskerrik Asko
 Misaotra Misaotra
 Rahmat Rahmat
 Matur Nuwun Matur Nuwun
 谢谢 谢谢
 Xbala Xbala
 Danke Danke
 Dhanyavadagalu Dhanyavadagalu
 Merci Merci
 Go Raibh Maith Agat Go Raibh Maith Agat
 Najis Tuke Najis Tuke
 Salamat Salamat
 ขอบคุณคุณคุณ ขอบคุณคุณคุณ
 Thank You Thank You