



# **AMD Together We Advance Financial Services**

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# The Alveo™ X3 Series

## Accelerating Your Trading Strategies

### Plug-and-Play Low Latency NIC

- Deterministic latency for reliable trade execution
- Plug-and-play with existing SW Stack

### CPU Offload for Hybrid Trading

- Offload compute-intensive functions closer to wire (NIC)
- Improve hit-ratio, transaction cost-analysis (TCA)

### Adaptable Accelerator for Custom Solutions

- Develop specialized Fintech solution in FPGA logic
- Customize in C/C++ or design in RTL for ultimate HW flexibility



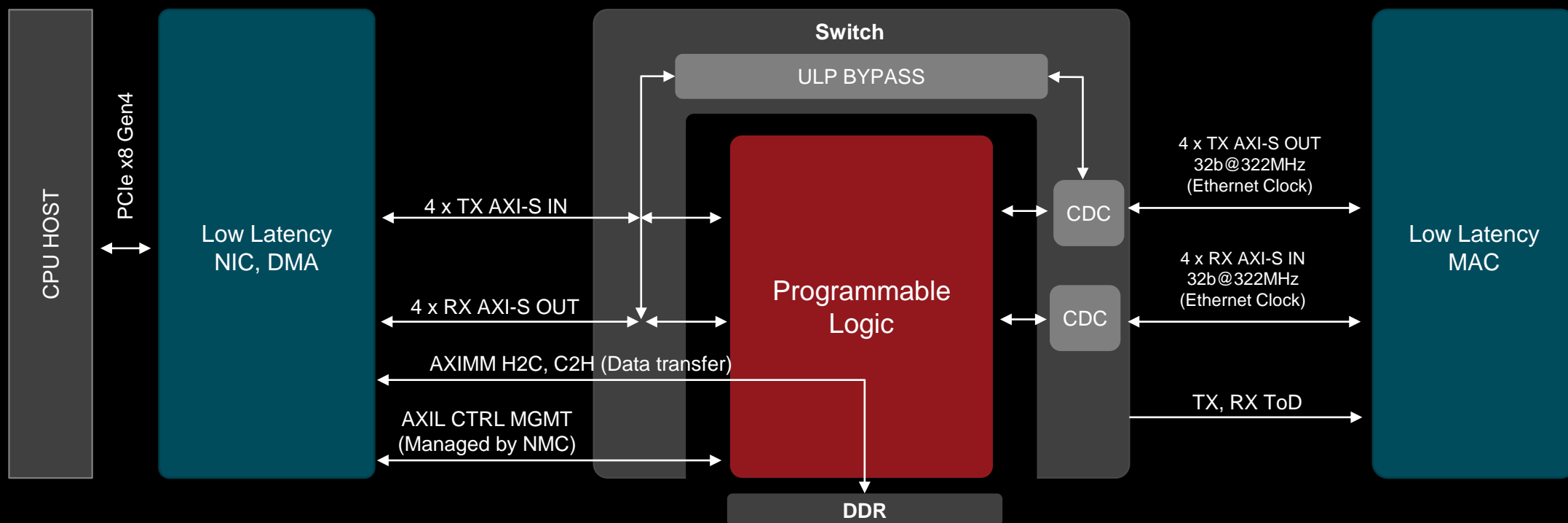
Interface	4x 10/25GbE
Logic Density	400K <sup>1</sup> – 1M <sup>2</sup> LUTs
Form Factor	HHHL
Max Power	75W

1: X3522 Low Latency NIC with hybrid upgrade package

2: X3522PV Accelerator Card

# Alveo X3522 Hybrid Mode Low Latency Shell\*

- Low latency shell remains static, providing functionality of the low latency NIC
- One Physical Function (PF) of NIC can be used by application to configure design in programmable logic
- Design with Vitis™ Unified Software Environment



\* Subject to change

# AMD EPYC™ EXCELS AT QUANTITATIVE ANALYTICS

## BETTER FULL MACHINE, PER-CORE, AND PER-WATT PERFORMANCE

In-house Full Machine QuantLib benchmark

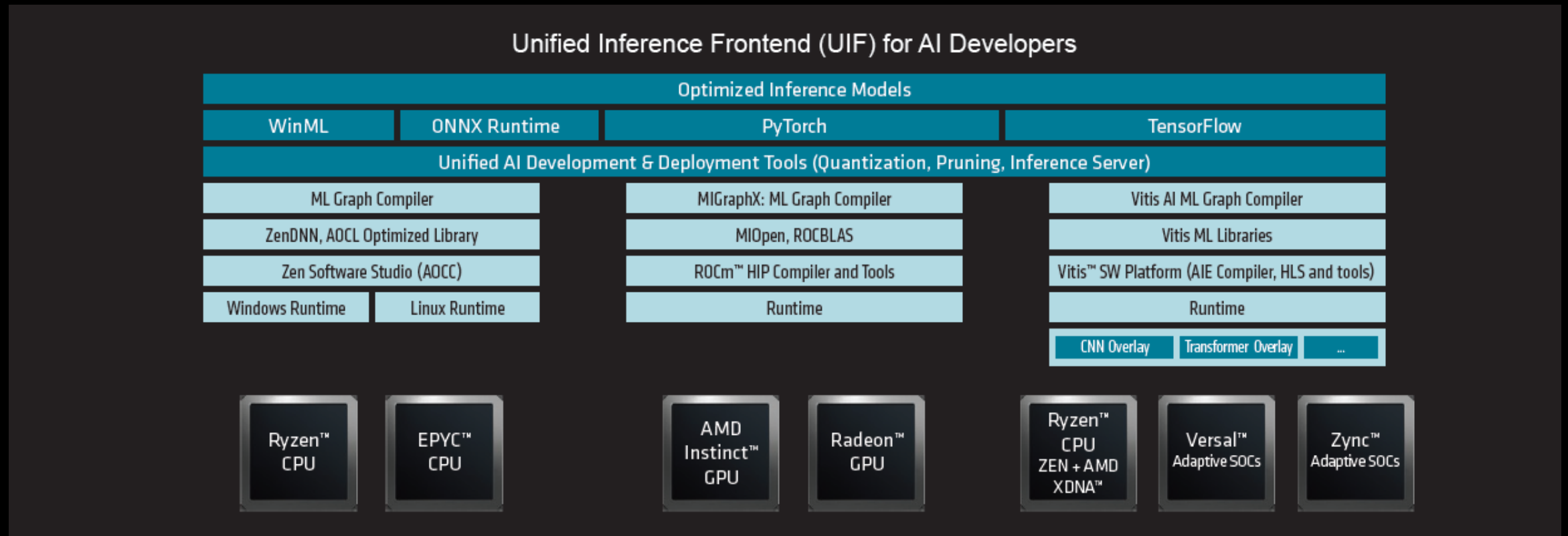


In-house Full Machine QuantLib benchmark calibrating models include Heston and HestonSLV and then pricing of a range of popular options including baskets, Bermudan Swaptions, Barriers, etc via MC/AMC and PDE methods.

Not STAC benchmarks

# EASY AI INFERENCE ACCELERATION WITH UNIFIED INFERENCE FRONTEND (UIF)

Fast AI inference is important for your business. The acceleration offered by various devices (CPUs, GPUs, FPGAs, ... ) is attractive, but you can't afford to learn all the tool chains/programming methods needed to port your models. You want to *train once, and run the inference on multiple hardware platforms*



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