

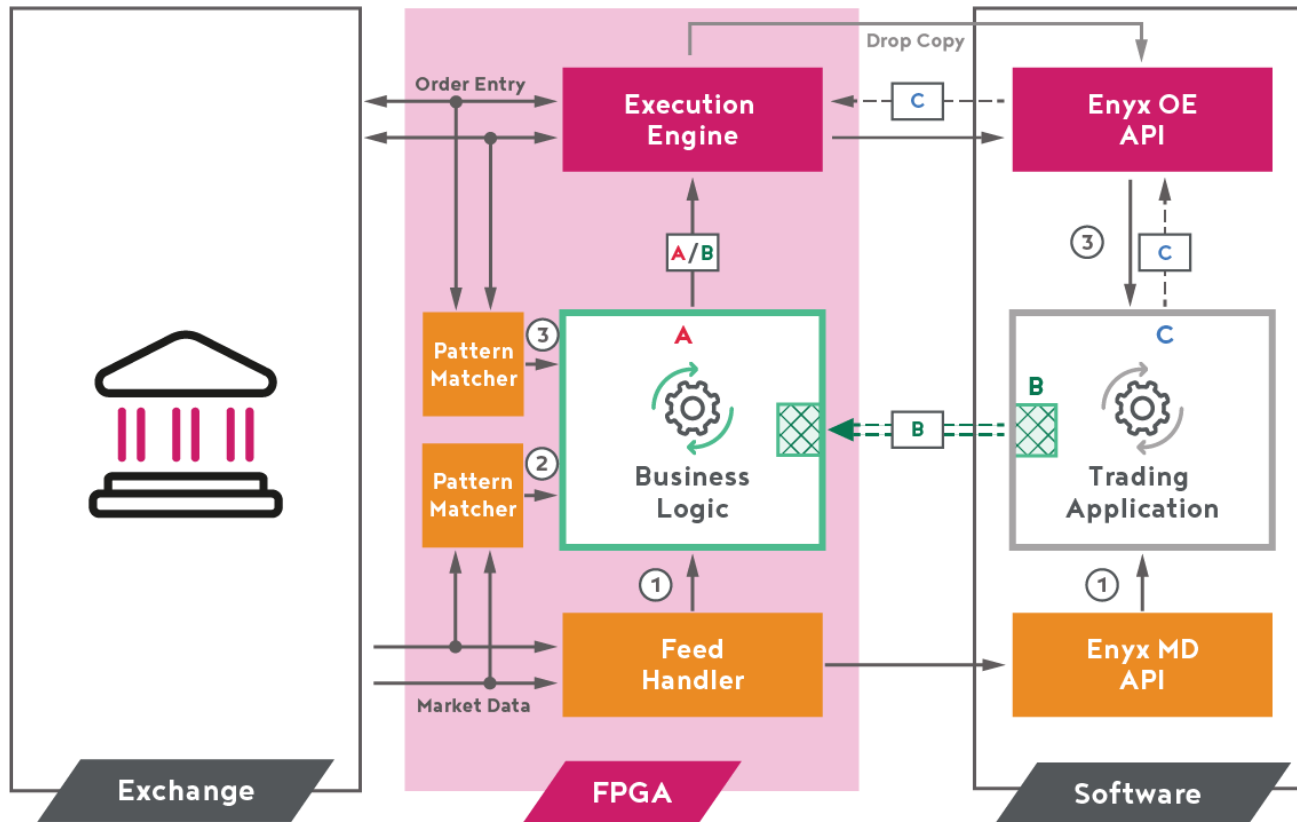


Do more with less:
How to trade using FPGA & get to market
faster without in-house expertise.

STAC – Spring 2023 – New York



A versatile tick-to-trade solution



Feature complete

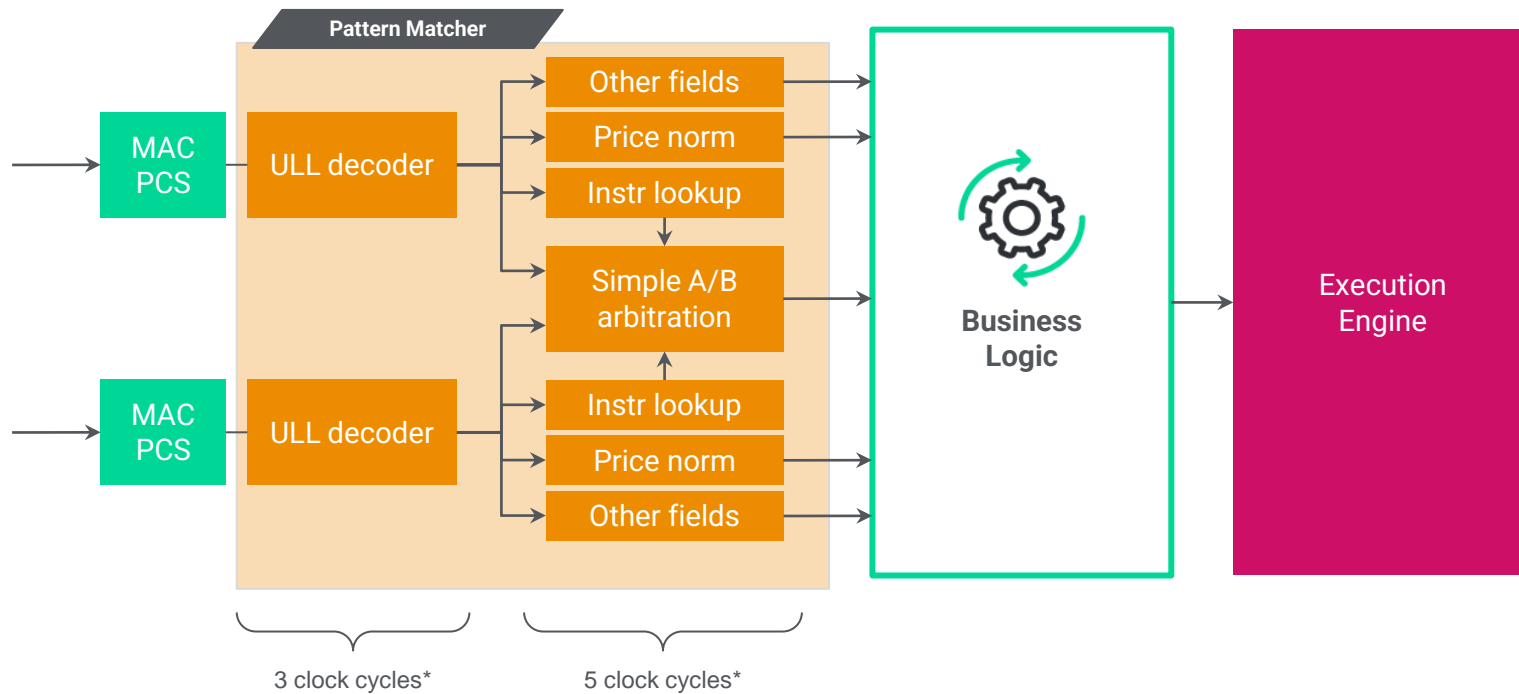
- Full FPGA book building with exchange resynchronization
- Customizable pattern matchers to extract data from raw protocols
- Full FPGA TCP management
- Embedded kill switch
- Parallel hardware & software datapaths

Use cases

- Fast cancel
- Market making
- Quantitative trading



Best performance without the pain



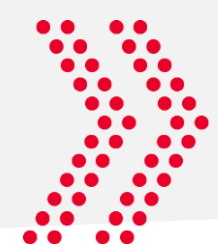
- Only process/outputs required fields/messages
- Tag/Value output
- Optional data processing:
 - Price normalization
 - Instrument lookup
 - Type conversion
 - A/B arbitration



Customizable Pattern Matcher

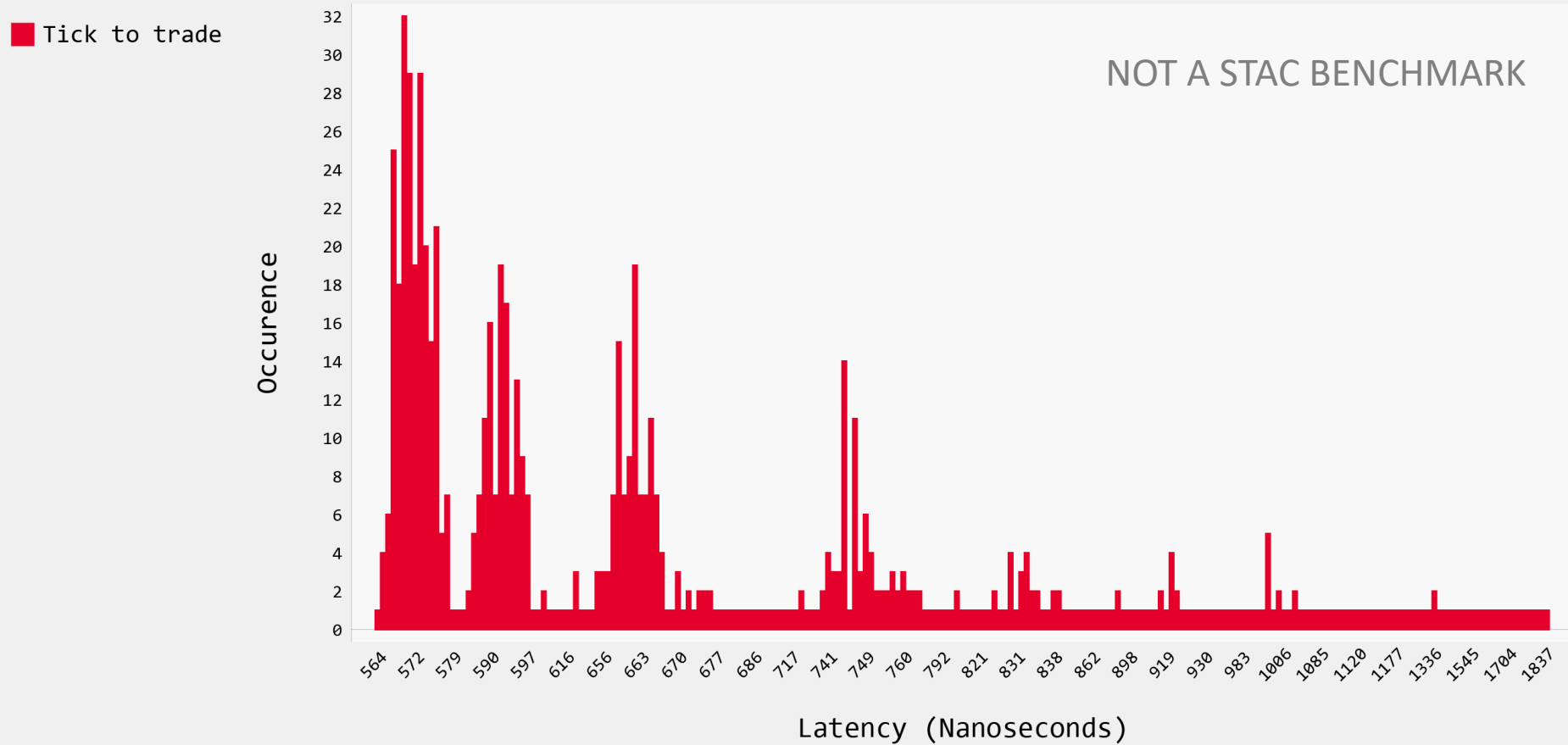
The screenshot shows a web browser window with the title "nxAccess Pattern Matcher" and the URL "localhost:5000". The page features the Exegy logo in the top left corner. Below the logo, there is a section titled "GENERATE PATTERN MATCHER SUPPORT" in red. This section contains a white box with two file upload options: "Choose Pattern Matcher configuration file" and "Choose Protocol description file". A red "Submit" button is located below these options. To the right of the white box, the text "nxAccess Pattern Matcher" and "Version: 069c882" is displayed. Below the "GENERATE PATTERN MATCHER SUPPORT" section, there is a section titled "MARKET DOCUMENTATION" in red. This section contains a dropdown menu with "CME MDP3" selected and a red "Download" button. At the bottom of the page, there is a footer with the text "© 2003-2023 All rights reserved. Exegy, Inc. | [Legal](#) | [Patents](#) | [Privacy Policy](#)".

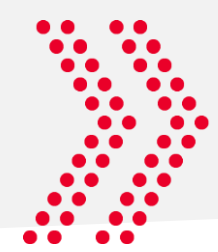
- SAAS solution to generate custom HDL code for your use case
- Upload simple configuration files to specify which fields and which messages the pattern matcher should process
- Enable/disable data processing features
- Configurable tag/value to allow for some level of normalization across venues



Hardware Latency – Feed Handler

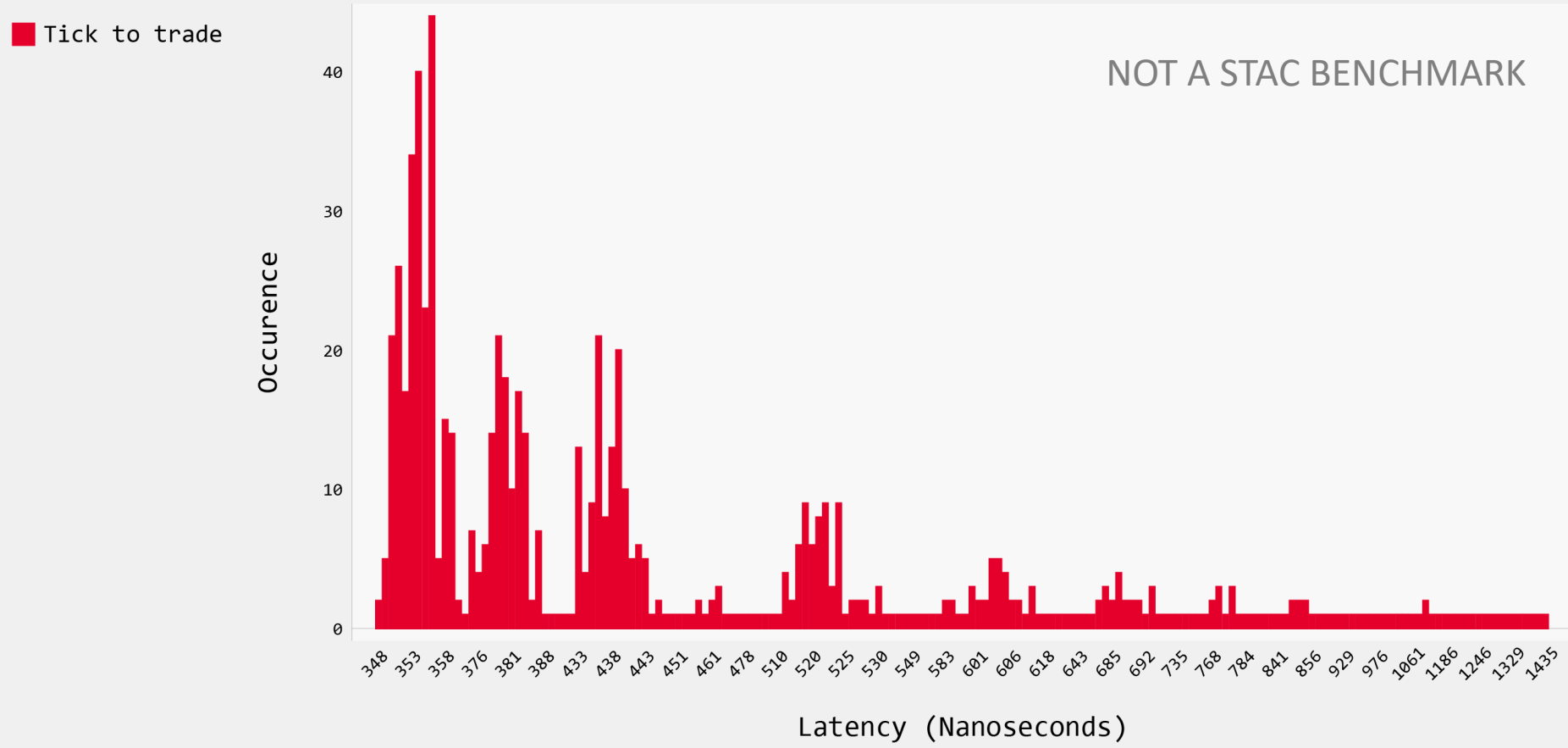
nxAccess CME - Feed Handler - Standard Collection - Wire to wire
Latency Distribution





Hardware Latency – Pattern Matcher

nxAccess CME - Pattern Matcher - Standard Collection - Wire to wire
Latency Distribution





Global Coverage

Equities

Futures

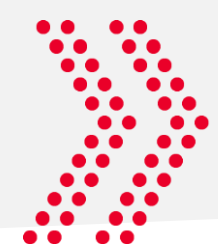
Options

Fixed Income

Cboe BYX
 Cboe BZX
 Cboe EDGA
 Cboe EDGX
 Cboe BZX Options
 Cboe EDGX Options
 Cboe C1
 Cboe C2
 Cboe Futures (CFE)
 CBOT
 CME
 COMEX
 NYMEX
 All Other Channels
 BrokerTec
 NYSE
 NYSE American
 NYSE Arca
 NYSE National
 ICE Futures US
 Nasdaq Stock Market
 Nasdaq BX
 Nasdaq PSX
 Nasdaq Fixed Income
 All US Options Venues
 Montréal Exchange

Cboe Europe Equities - BXE
 Cboe Europe Equities - CXE
 Cboe Europe Equities - DXE
 Eurex
 Xetra
 Euronext
 Euronext Amsterdam
 Euronext Brussels
 Euronext Dublin
 Euronext Lisbon
 Euronext London
 Euronext Paris
 Alternext Paris
 ICE UK LIFFE
 LME
 Borsa Italiana
 EuroTLX
 FTSE
 London Stock Exchange
 Trade Echo
 Turquoise
 SIX Swiss Exchange

HKEx Securities
 HKEx Derivatives
 Osaka Exchange (OSE)
 KOSPI
 KOSDAQ
 NSE
 TAIFEX



Straightforward onboarding

- Software simulation models for market data and order entry for development and **testing without the need for a FPGA card**
- Extensive documentation and reference designs
- Reference firmware allows for full solution testing without any hardware development

Thank you

Don't forget to tick the Exegy box for more information!

