

High Performance FPGA Market Data Solutions

We are ready for the new SIP

STAC - November 30, 2022

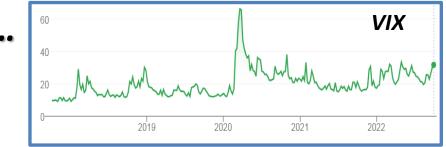
Pierre Gardrat, CTO

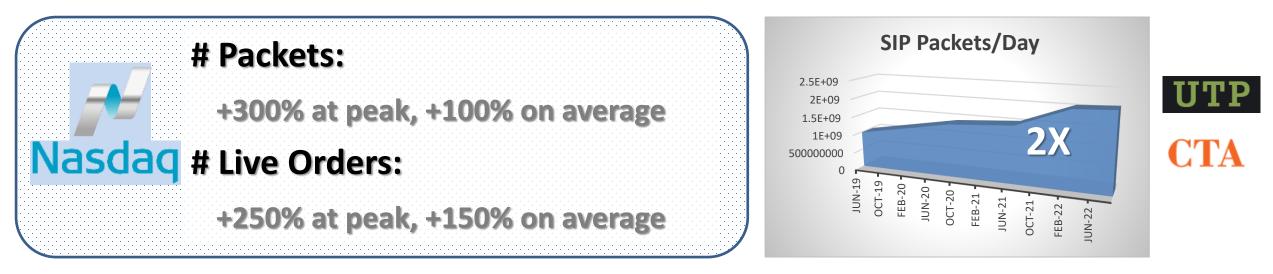
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Big Changes in US Equity Markets since COVID-19

More Instruments... More Traders... More Volatility...





10% Latency Reduction: January 2020 580ns -> January 2022 520 ns

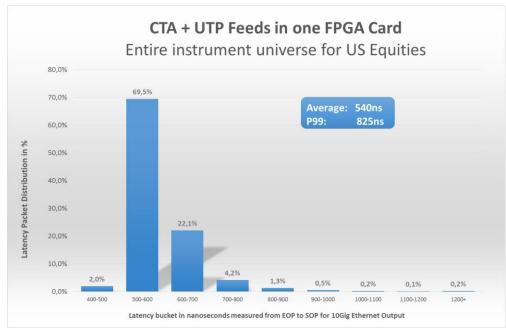
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SIP Feed Handlers

Synthetic SIP

- Support both CTA and UTP with the following characteristics:
 - Normalization of all the messages in NSWF,
 NSMDA C++ API or NovaLink hardware bus
- Footprint: **1 FPGA card**
 - Single Bay or 1 PCIe Card
- Performance:

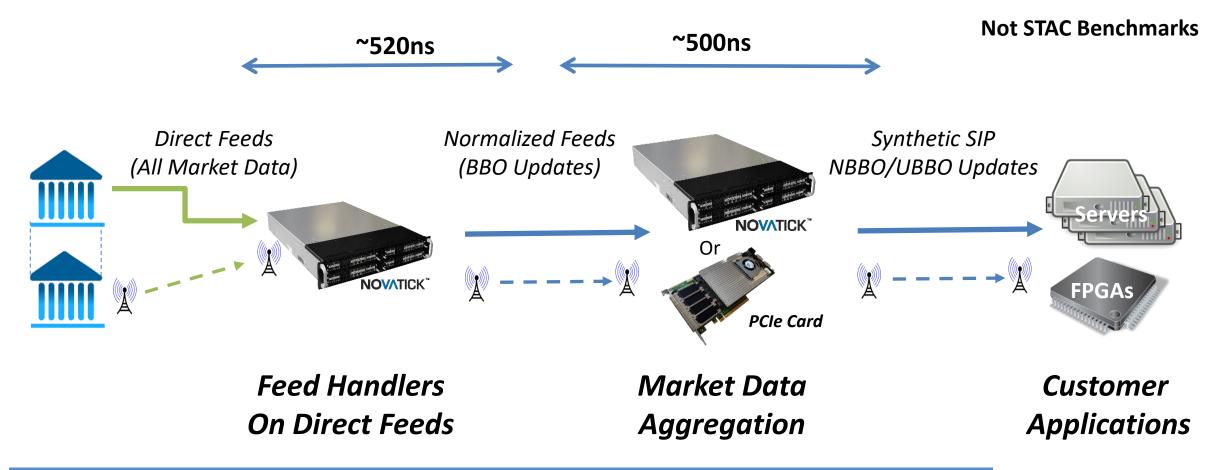


- Fast version of SIP with potentially different/additional content (depth, auction, odd lots...)
- Based on Direct Feeds from US exchanges
 - CBOE, Nasdaq and NYSE and regional or SIP feeds
- Footprint: **5 FPGA cards**
 - 1 fully loaded 2U appliance + 1 PCIe card
- Performance:
 - Feed Handling and Aggregation in ~1 micro-second on All US Equities
- Full customization of the quotation data consolidation rules
- In production since 2019

Not STAC Benchmarks



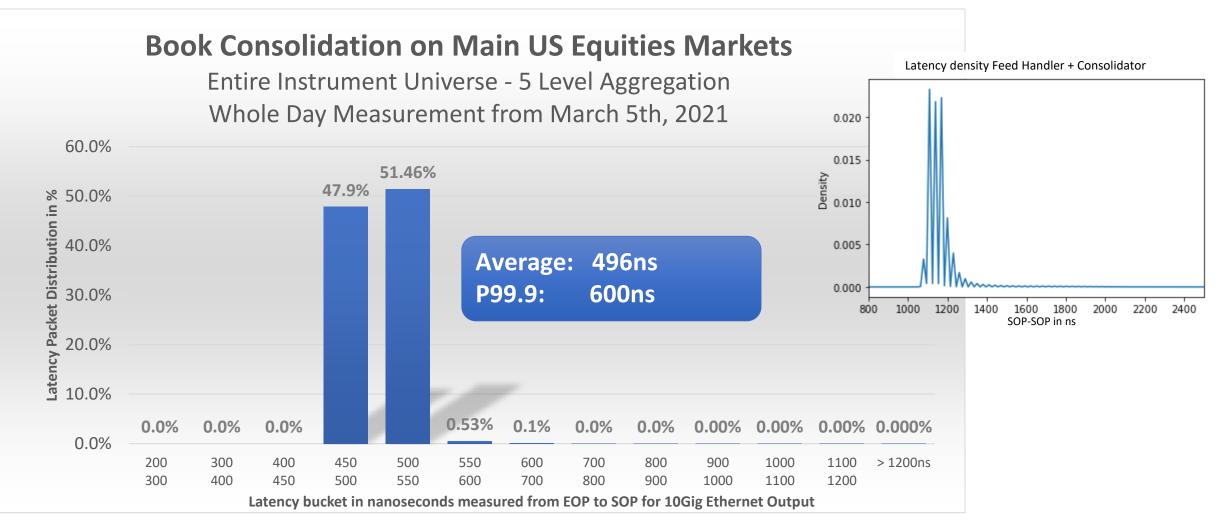
NOVASPARKS Synthetic SIP in ~1 micro-second



"Over the past year, we have used NovaSparks' U.S. Equities feed handlers and User Defined BBO consolidator to dramatically improve performance during market bursts resulting in consistently lower latencies during high volume periods. This remarkable innovation gives us the opportunity to focus our development efforts in other areas." Anand Prakash, Cutler Group



NOVASPARKS Synthetic SIP Aggregation Latency Profile



Not STAC Benchmarks



New SIP Content and Challenges

- Add 5 levels depth below/above NBBO, odd lots quote, Imbalance and Auction Data
- Based on direct feeds provided by US equities exchanges
 - Remove proprietary throttled TCP feeds
- New SIP distributed in all liquidity Centers (Carteret, Secaucus, Mahwah)
 - Find NBBO local to your trading venue
- New SIP market data volume is expected to outgrow current SIP volume by 3x-4x
 - Major challenges for new SIP feed handler vendors and new SIP providers, aka Competing Consolidators

FPGA solutions are required for scalability/resiliency/performance

We are ready for the new SIP!

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Check the NovaSparks box on your card if you would like to learn more about the SIP or our platform