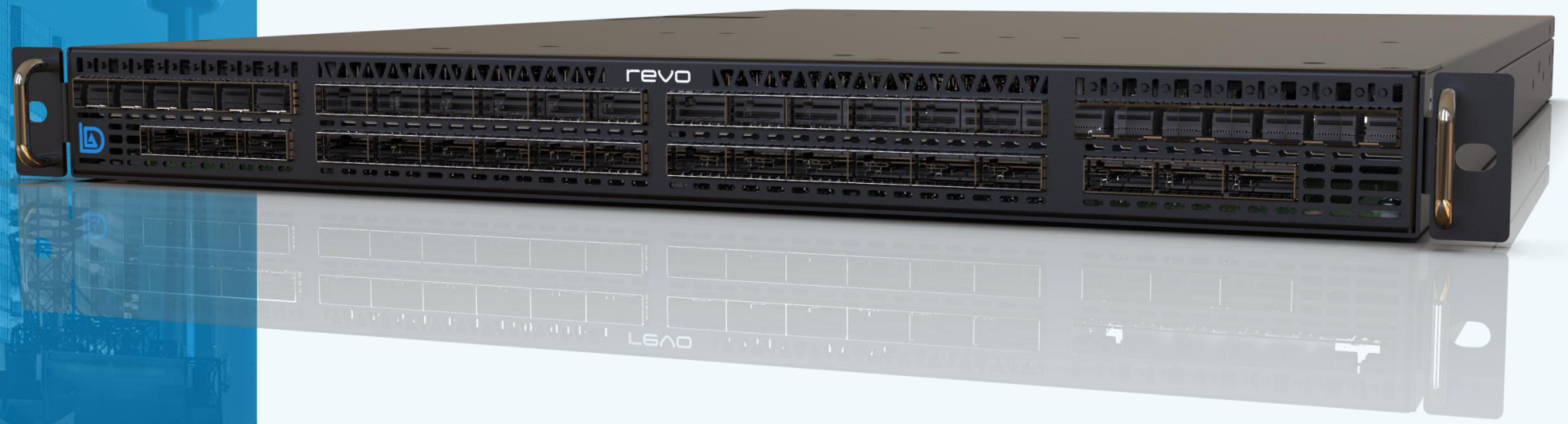
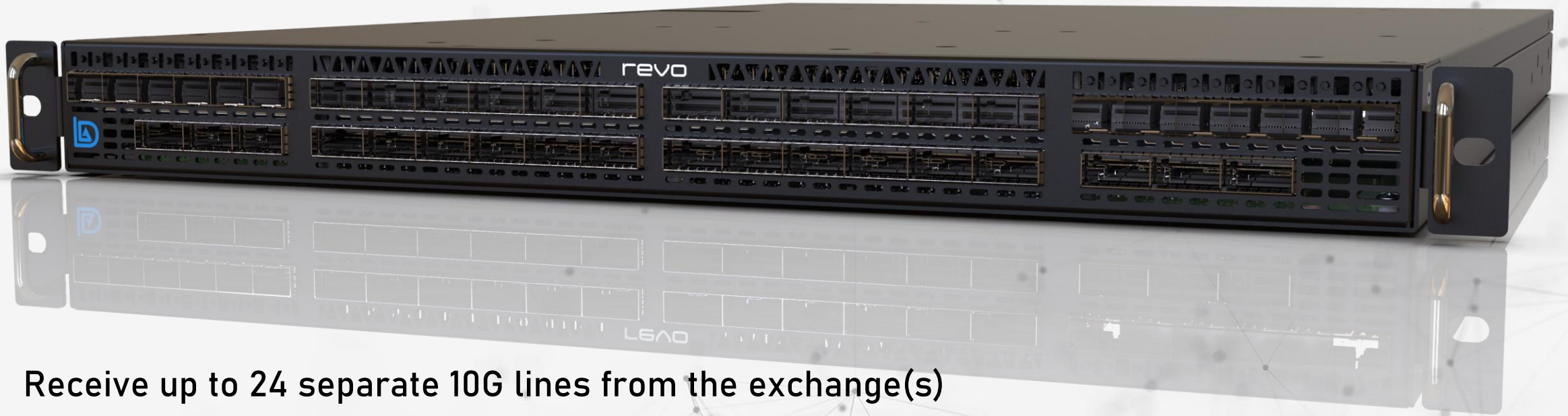
LDA TECHNOLOGIES™



Introducing
LDA Revo

A specialized high-density device for line arbitration

Use Case



- Receive up to 24 separate 10G lines from the exchange(s)
- Deliver the data from each line to 12 FPGA boards
- Enable each FPGA board to send data to any line
- **At least 312 (?!) 10G links: 24 + 24*12 = 312**

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LDA Revo

- 12x SFP+ ports for exchange connectivity
- 24x QSFPDD ports (12 port pairs) for downstream (FPGA board) connectivity
- 6x QSFPDD ports for housekeeping/management tasks or external timestamping/capture

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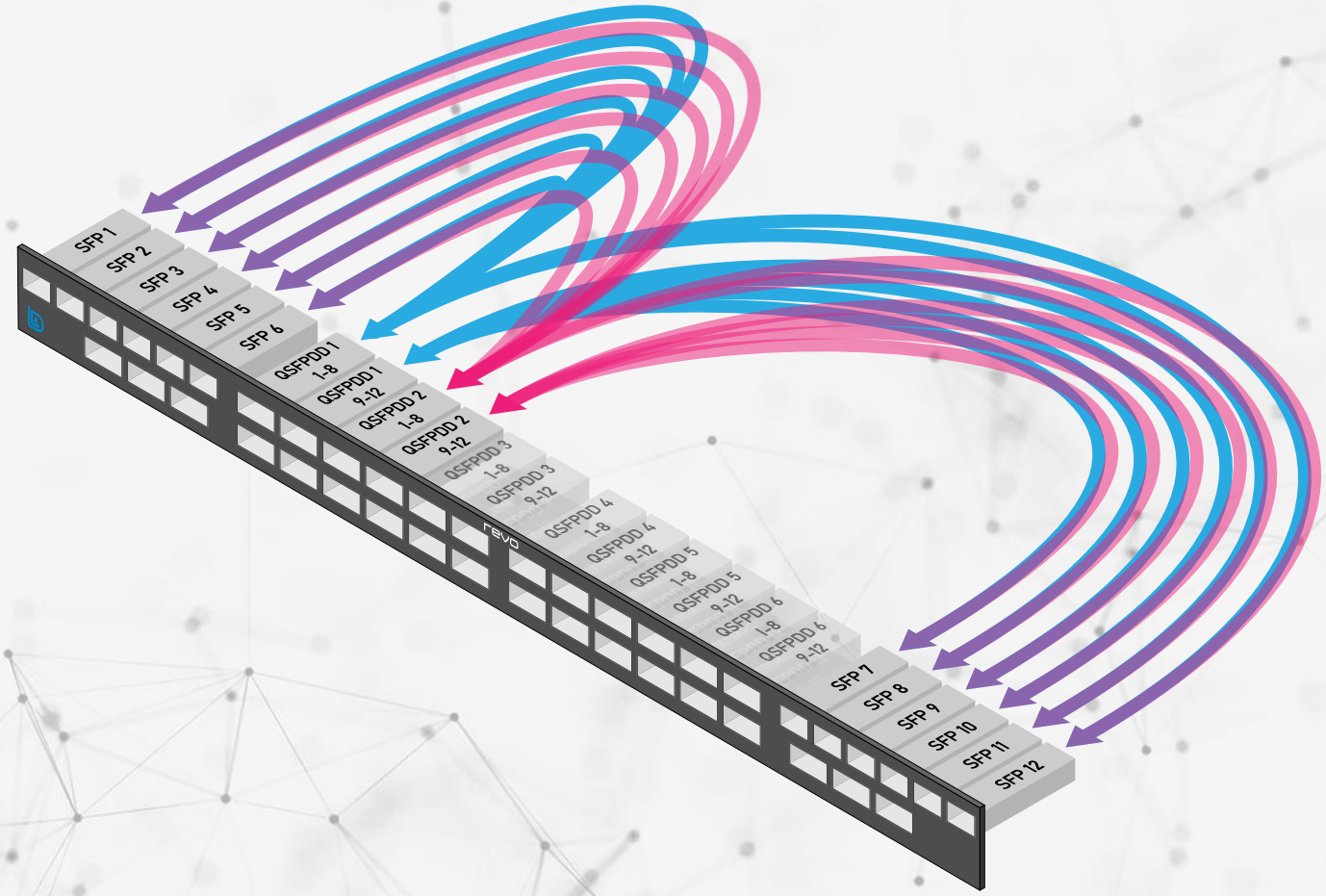
- Configurable Layer 1 for SFP+ output



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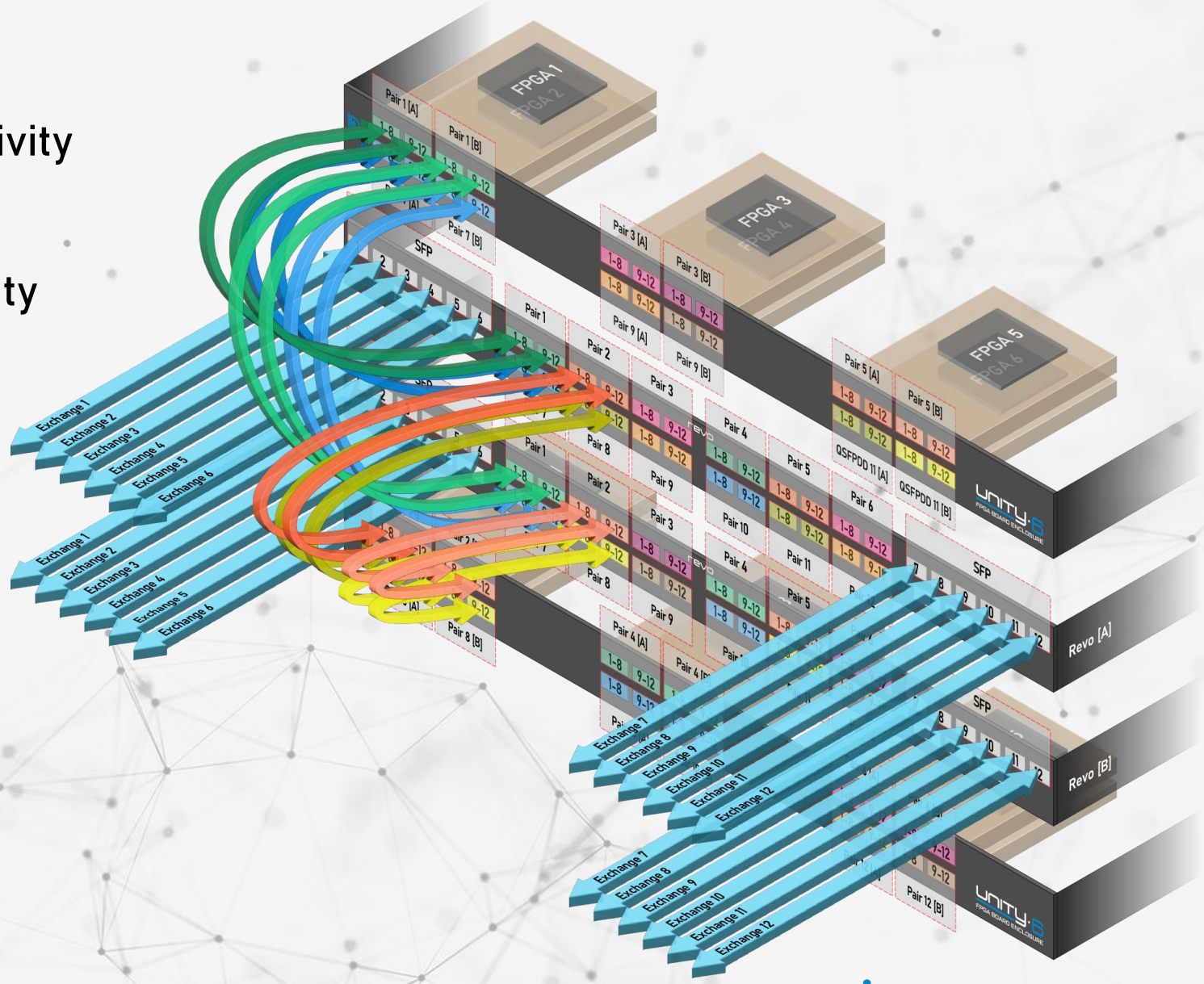
- Downstream interconnect with 2"-7" QSFPDD copper cables



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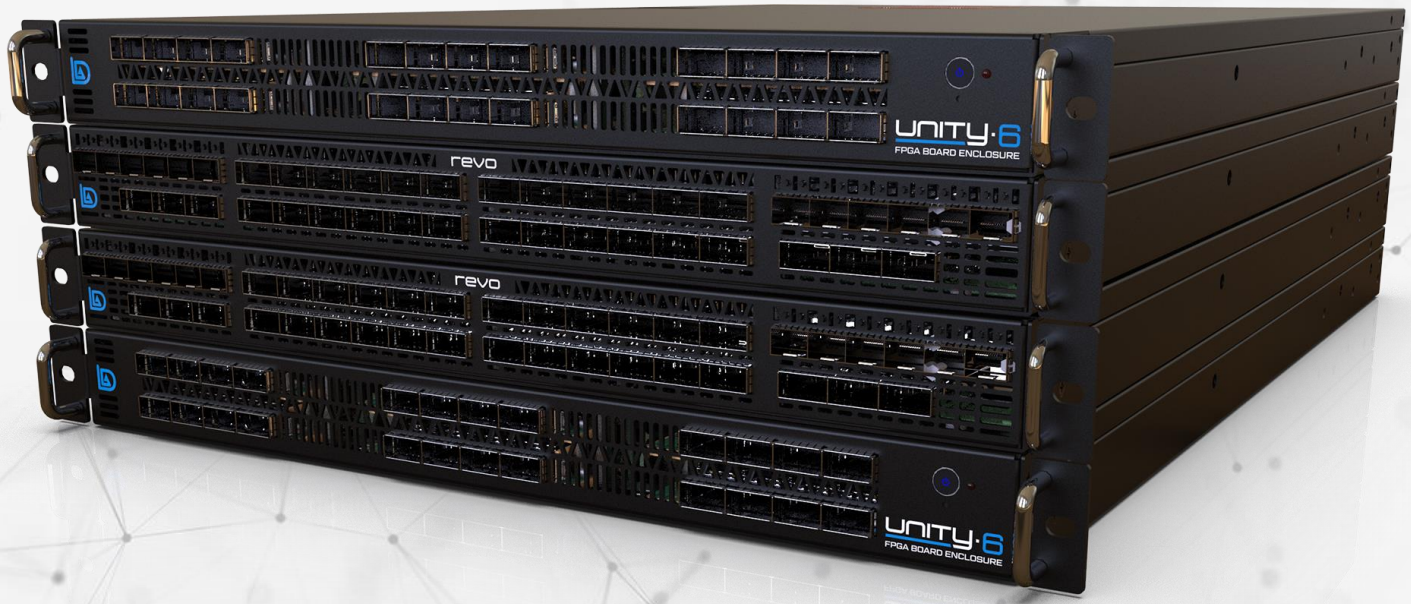
- Internal tap aggregation and timestamping option with the complete LDA NeoTap features for capture and monitoring purposes.



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