FPGAS ACCELERATING AL FOR FINANCIAL SERVICES

Intel[®] Network & Custom Logic Group (NCLG)

Intel[®] AI Hardware – Device, Edge, and Multi-Cloud OPTIMIZED SOFTWARE STACK ACCELERATORS CPU **FPGA** GPU (intel) (intel) (intel) intel inte inte **FPGA** XEON GPU NERVANA NERVANA MOVIDIUS PLATINUM **10TH GE** inside inside inside' inside NNP-I NNP-T JOTH GEL WORKLOAD BREADTH **AI SPECIALIZATION**

Multi-purpose foundation for artificial intelligence (AI) Real-time deep learning inference and more

Highly-parallel media, graphics and compute

Multi-modal deep learning inference

Edge media and vision inference

Deep learning training

www.intel.ai/technology

All products, computer systems, dates, and figures are preliminary based on current expectations, and are subject to change without notice. ¹Unified software stack development in progress

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RNNs for Financial Applications

Recurrent neural networks (RNNs) are neural networks with memory



Fraud detection

Anti-money laundering

Speech recognition

Requirements

- Low latency for real-time response
- High memory-to-compute ratio for increased performance





Intel[®] FPGAs Well Suited to Address RNN Workloads

Intel[®] FPGAs enabling technologies

- Pipelining
- Many large independent local memories
- Independent DSP

What this means for RNN applications:

Delivering batch 1 performance 54 TOPs at 34.9 W

High memory bandwidth

Up to 58TB/s

Delivering unstructured sparsity 96% sparsity

If you would like to hear more, stop by our stand today



RNN Demo at Booth

	Myrtle results	
Platform	Intel® FPGA PAC D5005 ¹	
Sparsity (%)	96	
Batch Size	1	
Effective Throughput (TOPS)	54.0	
Power (W)	34.9	
Performance per Watt (Effective GOPS/W)	1547	
Latency per 1s input audio (mS)	0.343	

Speech Transcription

This demo showcases Speech to Text conversion using Myrtle's Recurrent Neural Network accelerator running on an Intel® Stratix® 10 FPGA. <u>Details</u>

Inference stream		On demand in	On demand inference	
Ŏ 49.93 TOPS	🔁 3843.5X	₩ 35 W	¥ 4.65% WER	
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 so for the hundredth tin slowly down through the so for the hundredth tin slowly down through the 	ne she was thinking today as e bottoms ne she was thinking to day a e bottoms	s she walked alone up the lane bar is she walked alone up the lane ba	ck of the barn and then ck of the barn and then	
 the analysis of knowledge our whole enterprise the analysis of knowledge our whole enterprise 	ge will occupy us until the en ge will occupy us until the en	nd of the thirteenth lecture and is nd of the thirteenth lecture and is	the most difficult part of the most difficult part of	
thought the fir tree and thought the fir tree and	believed it all because the r believed it all because the r	man who told the story was so goo man who told the story was so goo	od looking well well	

Not STAC Benchmarks

- 1. Intel® Programmable Acceleration Card (Intel PAC) measurements taken in conjunction with Intel i7-7700K at 4.20 GHz, RAM 4 * 16 GB at 2,800 MHz, 1 TB M.2 PCIe* SSD, PRIME Z270-P motherboard, 650 W PSU, Ubuntu
- 2. Peak throughput of 53.37 TOPS measured over shorter input duration of 200 ms, When measuring latency over a 1s input period, peak throughput drops to 23 TOPS

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