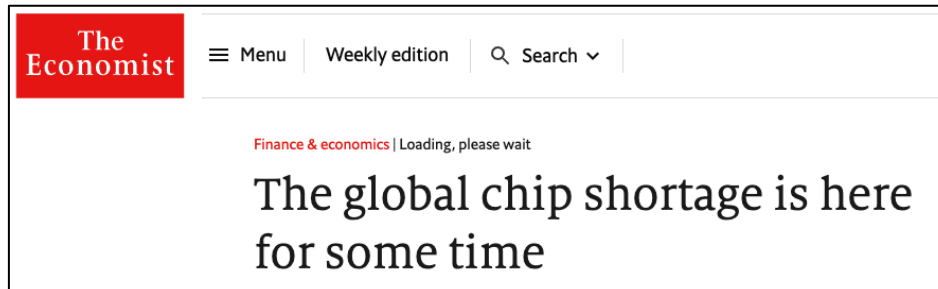# STAC Update:
Fast Data

Peter Nabicht
President, STAC

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Audits update

- Multiple SUTs were under development

- Supply chain disruptions impacted readiness timelines
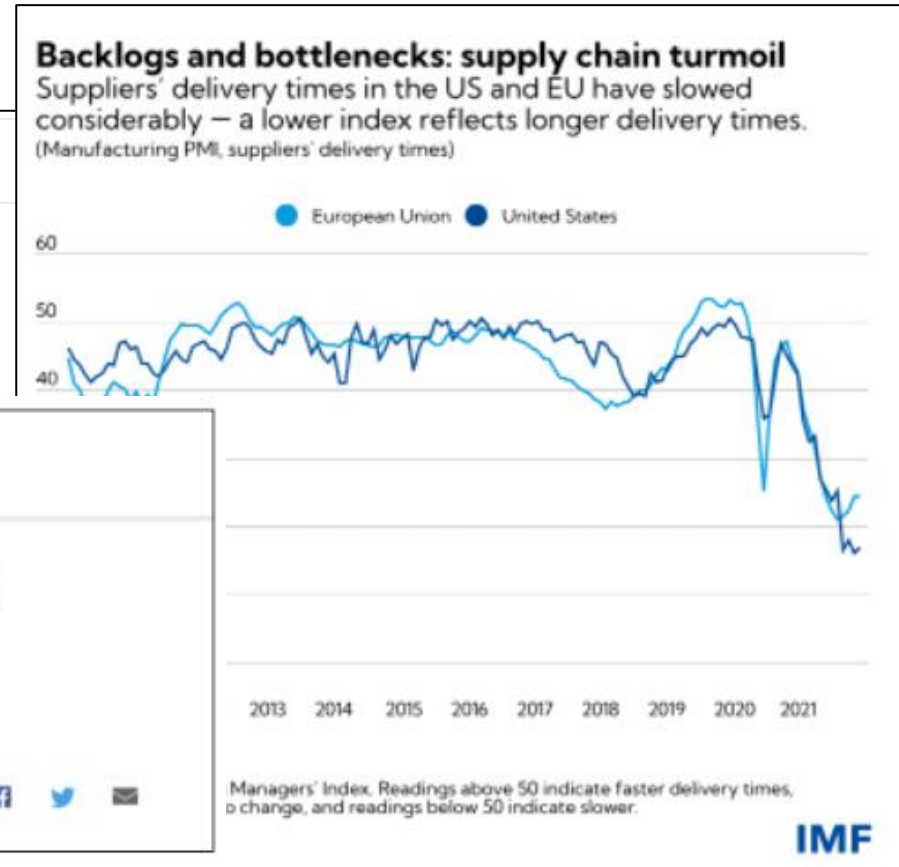
FPGA SIG update

- Vendors and financial firms meeting
  - Exploring ways to collaborate on tool chain and development workflows

- Next meeting being scheduled for end of May / early June

- Not too late to join us

http://www.stacresearch.com/fpga

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Adding STAC-ML Markets (Inference)

- ## STAC-ML Markets (Inference)

  - Benchmark solutions that may be used to run inference on realtime market data
  - Reports performance, resource, efficiency, and quality

  - Benefits derived from FPGA (latency vs throughput vs determinism)
  - Broad categories of use

- ## Premium subscribers have access to:

  - Benchmark specifications
  - Configuration Disclosures
  - Extensive, deeper visualizations of audited and un-audited report
  - Vault Reports
  - Research notes

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Cloud connectivity latencies

- Have had increasing interest in understanding:
  - On-prem to cloud latencies
  - Cloud-to-cloud latencies

- Some interesting details to work out, for example:
  - Measuring into user space to include full cloud network stack?
  - With multiple zones within a region, what is the right endpoint?

- If interested in helping define requirements:

  council@STACresearch.com