



The 5 biggest misconceptions regarding low latency trading technology

STAC Fall Chicago

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1. Only HFTs need FPGAs

FPGA technology is not only for latency.

- FPGA tech is great for latency when applicable -- also alleviates issues with data volumes & determinism
- Leverage FPGAs to extend your infrastructure capacity & limit your maximums
- Reduce overall footprint (ie power consumption & server rack usage)
- Requires less servers to digest the equivalent amount of data compared to pure software solutions

Be smarter and faster.

2. I've only used software, so FPGA technology is too difficult

Upgrading your technology doesn't have to be overwhelming or high risk

- Benefit from using FPGAs without necessarily having hardware expertise
- Use a stepwise approach only move what is necessary into FPGA
- Scale to multiple venues with a team of one or two hardware engineers

Don't reinvent the wheel, focus on what makes you different in the market



Different strategies need different level of market data processing

- Pattern matcher looking into raw data for ULL
- Ticker plants for fully normalized & aggregated view of the market
- Predictive signals for market trends & analytics

Use the right tool for the right job

4. Using a vendor solution is giving up control

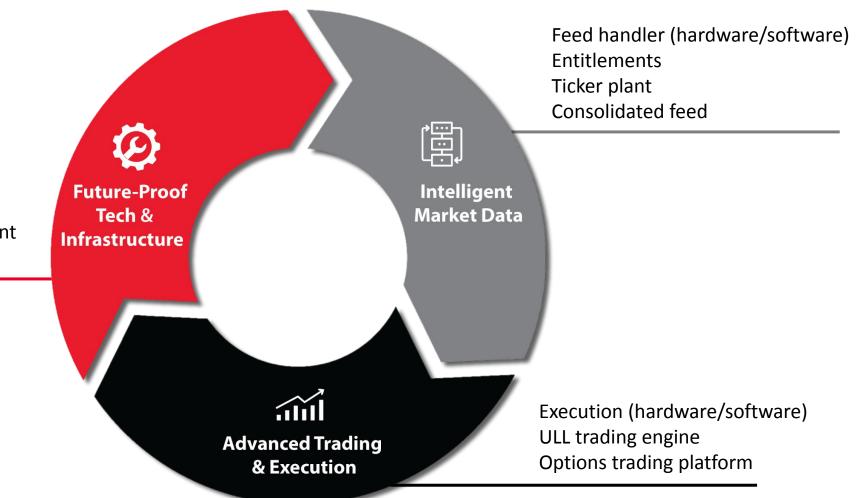
The vendor model has changed -- partners in technology, allowing for **both** flexible time-to-market & control

- Solutions are no longer black boxes: Open the box when needed -- technology needs to adapt to customer needs, not the reverse.
- Buy to Build model: Ability to give your teams what they need -- from technology components to full bespoke solutions.
- Vendor engagement models have evolved: Get the help you need where you need it -- from technology consultants to fully managed services.



5. Choose a partner not a vendor

FPGA development framework Bandwidth & redundancy management Data center operations



Extension of your team & Partner in innovation

Intelligent Data

Hardware Acceleration

User-focused approach

Signum delivered via **Axiom** and **XTP** Realtime, intra, & end of day analytics for quants Iceberg calculation, quote direction & duration, sentiment

FPGA tick-to-cancel for **DMA** & **Metro** trading platforms Performance & reliability improvement across Exegy's offering

Breaching the gap between control and agility Multi-platform hardware libraries Customizable hardware design

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